

IN THE CLAIMS

No claims are presently canceled, added, or amended. For the Examiner's convenience, the text of all pending claims, along with their current status, is set forth below.

1. (Original) A method of testing semiconductor die comprising the acts of:
forming a stack of at least two semiconductor die; and
after the stack is formed, testing the semiconductor die in the stack prior to attaching the semiconductor die to a packaging substrate.
2. (Original) The method, as set forth in claim 1, where the act of forming comprises the acts of:
 - a) picking up a first die having a topside and an underside with a die picking tool;
 - b) applying adhesive to the underside of the first die, thereby providing an adhesively coated underside of the first die; and
 - c) without releasing the first die from the die picking tool, picking up a second die having a topside and an underside by placing the adhesively coated underside of the first die against the topside of the second die, thereby forming a die stack.
3. (Original) The method, as set forth in claim 2, wherein the first die is thicker than the second die.
4. (Original) The method, as set forth in claim 2, wherein the acts (a), (b), and (c) are performed in the recited order.

5. (Original) The method, as set forth in claim 2, wherein act (b) is performed before act (a).

6. (Original) The method, as set forth in claim 2, comprising the act of applying adhesive to the underside of the second die, thereby providing an adhesively coated underside of the second die.

7. (Original) The method, as set forth in claim 6, comprising the act of without releasing the first die from the picking tool, picking up a third die having a topside and an underside by placing the adhesively coated underside of the second die against the topside of the third die.

8. (Original) The method, as set forth in claim 1, wherein the act of forming comprises the act of forming a stack of at least three semiconductor die.

9. (Original) The method, as set forth in claim 1, wherein the act of tesing comprises the act of:

after the stack is formed, functionally testing the semiconductor die in the stack prior to attaching the semiconductor die to the packaging substrate.

10. (Original) The method, as set forth in claim 1, wherein the act of testing comprises the act of:

after the stack is formed, environmental testing the semiconductor die in the stack prior to attaching the semiconductor die to the packaging substrate.

11. (Original) The method, as set forth in claim 1, comprising the act of coupling the tested stack to the packaging substrate.

12. (Original) The method, as set forth in claim 1, wherein packaging substrate comprises a tape reel.

13. (Original) The method, as set forth in claim 1, wherein the packaging substrate comprises a film frame.

14. (Original) The method, as set forth in claim 1, wherein the packaging substrate comprises a gel pack.

15. (Original) The method, as set forth in claim 1, wherein the packaging substrate comprises a wafer.

16. (Original) The method, as set forth in claim 1, comprising the acts of:
applying a first adhesive between each of the at least two semiconductor die, the first adhesive being curable at a first temperature; and
applying a second adhesive between the stack and the packaging substrate, the second adhesive being curable at a second temperature lower than the first temperature.

17. (Original) The method, as set forth in claim 1, wherein the act of forming comprises forming a shingle stack.

18. (Original) The method, as set forth in claim 11, comprising the act of using the tested stack coupled to the packaging substrate to form an integrated circuit package.

19. (Original) The method, as set forth in claim 18, comprising the act of electrically coupling the integrated circuit package to a processor to form an electronic system.

20. (Original) The method, as set forth in claim 1, wherein at least one of the at least two semiconductor die comprises a memory die.